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IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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1. Design of digital filters using simulated annealing

Kacelenga, R.V.; Graumann, P.J.; Turner, L.E.;
[Circuits and Systems, 1990. IEEE International Symposium on](#)
1-3 May 1990 Page(s):642 - 645 vol.1
Digital Object Identifier 10.1109/ISCAS.1990.112147

[AbstractPlus](#) | Full Text: [PDF](#)(396 KB) IEEE CNF
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2. DSP system synthesis including variable data path width

Johnston, B.A.; Graumann, P.J.; Turner, L.E.;
[Circuits and Systems, 1994. ISCAS '94. 1994 IEEE International Symposium on](#)
Volume 1, 30 May-2 June 1994 Page(s):53 - 56 vol.1
Digital Object Identifier 10.1109/ISCAS.1994.408753

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IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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☐ 1. Implementation of low-power FFT processor cores using a novel order-based processing scheme

Hasan, M.; Arslan, T.;

[Circuits, Devices and Systems, IEE Proceedings-](#)

Volume 150, Issue 3, 6 June 2003 Page(s):149 - 154

Digital Object Identifier 10.1049/ip-cds:20030347

[AbstractPlus](#) | Full Text: [PDF](#)(452 KB) IEE JNL

☐ 2. A hardware efficient control of memory addressing for high-performance FFT processors

Ma, Y.; Wanhammar, L.;

[Signal Processing, IEEE Transactions on \[see also Acoustics, Speech, and Signal Processing, IEEE Transactions on\]](#)

Volume 48, Issue 3, March 2000 Page(s):917 - 921

Digital Object Identifier 10.1109/78.824693

[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(152 KB) IEEE JNL

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| #4 | (graumann p. j.<in>au) | 2 |
| #5 | (graumann p. j.<in>au) | 2 |
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| L6 | 213 | 5 and "708"/\$.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/01/13 12:38 |
| L7 | 83 | 6 and 708/404.ccls. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/01/13 12:38 |
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| L9 | 24 | ("20020083107" "20020178194" "5028877" "5091875" "5371696" "5694347" "5717620" "5831883" "5890098" "6081821" "6324561" "6408319").PN. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/01/13 14:32 |
| L10 | 24 | ("20020083107" "20020178194" "5028877" "5091875" "5371696" "5694347" "5717620" "5831883" "5890098" "6081821" "6324561" "6408319").PN. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/01/13 14:34 |
| L11 | 377 | 708/400-409.ccls. and butterfl\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/01/13 14:34 |
| L12 | 311 | 11 and (twiddle or coefficient\$1) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/01/13 14:35 |
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| L16 | 0 | ("2004/0059766").URPN. | USPAT | OR | ON | 2007/01/13 14:38 |
| L17 | 0 | (10/065154).APP. | USPAT; USOCR | OR | ON | 2007/01/13 14:38 |

Inventor Information for 10/760379

| Inventor Name | City | State/Country |
|----------------------|---------|---------------|
| GIBB, SEAN G. | CALGARY | CANADA |
| GRAUMANN, PETER J.W. | CALGARY | CANADA |

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First Name = SEAN

| Application# | Patent# | Status | Date Filed | Title | Inventor Name |
|--------------------------|------------|--------|------------|--|---------------|
| 60487975 | Not Issued | 159 | 07/18/2003 | Recoded radix-2 pipelined FFT processor | GIBB, SEAN |
| 60524879 | Not Issued | 159 | 11/26/2003 | Pipelined FFT processor with memory address interleaving | GIBB, SEAN |
| 10760379 | Not Issued | 30 | 01/21/2004 | Recoded radix-2 pipeline FFT processor | GIBB, SEAN G. |
| 10844316 | Not Issued | 30 | 05/13/2004 | Pipelined FFT processor with memory address interleaving | GIBB, SEAN G. |
| 10844486 | Not Issued | 41 | 05/13/2004 | Interleaving memory | GIBB, SEAN G. |
| 29033216 | Not Issued | 161 | 12/22/1994 | STRIPED FURNITURE FABRIC | GIBBONS, SEAN |
| 29033217 | Not Issued | 161 | 12/22/1994 | STRIPED FURNITURE FABRIC | GIBBONS, SEAN |
| 29108493 | Not Issued | 161 | 07/28/1999 | LOUDSPEAKER SPIDER GASKET | GIBBONS, SEAN |
| 29108494 | Not Issued | 161 | 07/20/1999 | LOUDSPEAKER FRAME COVER | GIBBONS, SEAN |

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|--------------------------|-------------------------|--------|------------|--|----------------------|
| 60487975 | Not Issued | 159 | 07/18/2003 | Recoded radix-2 pipelined FFT processor | GRAUMANN, PETER |
| 60524879 | Not Issued | 159 | 11/26/2003 | Pipelined FFT processor with memory address interleaving | GRAUMANN, PETER |
| 10156292 | 7139312 | 150 | 05/23/2002 | SYSTEM AND METHOD FOR IMPROVING CODING GAIN PERFORMANCE WITHIN GIGABIT PHY VITERBI DECODER | GRAUMANN, PETER J. |
| 11479871 | Not Issued | 30 | 06/29/2006 | System and process for packet delineation | GRAUMANN, PETER J. |
| 10760379 | Not Issued | 30 | 01/21/2004 | Recoded radix-2 pipeline FFT processor | GRAUMANN, PETER J.W. |
| 10844316 | Not Issued | 30 | 05/13/2004 | Pipelined FFT processor with memory address interleaving | GRAUMANN, PETER J.W. |
| 10844486 | Not Issued | 41 | 05/13/2004 | Interleaving memory | GRAUMANN, PETER J.W. |

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